Listing of Claims

The following listing of claims replaces all prior versions and listings of claims in this application.

1. – 13. (previously canceled)

14. (currently amended) An optical receiver circuit, comprising:

a differential amplifier including a first input and a second input;

an optical reception device connected to said first input of said differential amplifier by a first preamplifier said optical reception device having an electrical behavior in an illumination-free case;

an electrical element for simulating the electrical behavior of said optical reception device in the illumination-free case, said electrical element connected to said second input of said differential amplifier by a second preamplifier; and

said first preamplifier and said second preamplifier being identical user-settable transimpedance amplifiers.

15. (previously canceled)

16. (previously presented) The optical receiver circuit according to claim 14, wherein:

said electrical element is formed by a darkened, further reception device.

17. (previously presented) The optical receiver circuit according to claim 16, wherein:

said optical reception device and said further reception device are monolithically integrated on a chip.

18. (previously canceled)

19. (previously canceled)

20. (previously presented) An optical receiver circuit, comprising:

a differential amplifier including a first input and a second input;

an optical reception device connected to said first input of said differential amplifier by a first preamplifier, said optical reception device having an electrical behavior in an illumination-free case;

an electrical element for simulating the electrical behavior of said optical reception device in the illumination-free case, said electrical element connected to said second input of said differential amplifier by a second preamplifier; and

said first preamplifier and said second preamplifier being identical;

an integrated control circuit;

said first preamplifier being a transimpendance amplifier having a feedback impedance with a magnitude being settable by a user via said integrated control circuit; and

said second preamplifier being a transimpedance amplifier having a feedback impedance with a magnitude being settable by a user via said integrated control circuit.

21. (previously presented) The optical receiver circuit according to claim 20, wherein:

said integrated control circuit is connected symmetrically to said feedback impedance of said first amplifier and to said feedback impedance of said second amplifier.

22. (previously presented) The optical receiver circuit according to claim 14, wherein:

said optical reception device and said electrical element are connected to a common supply voltage.

23. (previously presented) The optical receiver circuit according to claim 22, further comprising:

a low-pass filter connected to the common supply voltage.

24. (previously presented) The optical receiver circuit according to claim 14, wherein:

said optical reception device is a photodiode; and

said electrical element is a photodiode.

25. (currently amended) The optical receiver circuit according to claim 14, further comprising:

a package for packaging said differential amplifier, said optical reception device, and said electrical element, said package being selected from the group consisting of a TO-46 package, a TSSOP10 package, and a VQFN20 package.

26. (currently amended) The optical receiver circuit according to claim 25, further comprising:

an integrated control circuit having a control terminal, said package having a terminal pin forming said control terminal.

27. (previously presented) The optical receiver circuit according to claim 20, wherein:

said electrical element is formed by a darkened, further reception device.

28. (previously presented) The optical receiver circuit according to claim 27, wherein:

said optical reception device and said further reception device are monolithically integrated on a chip.

29. (previously presented) The optical receiver circuit according to claim 20, wherein:

said optical reception device and said electrical element are connected to a common supply voltage.

30. (previously presented) The optical receiver circuit according to claim 29, further comprising:

a low-pass filter connected to the common supply voltage.

31. (previously presented) The optical receiver circuit according to claim 20, wherein:

said optical reception device is a photodiode; and

said electrical element is a photodiode.

32. (currently amended) The optical receiver circuit according to claim 20, further comprising:

a package for packaging said differential amplifier, said optical reception device, and said electrical element, said package being selected from the group consisting of a TO-46 package, a TSSOP10 package, and a VQFN20 package.

33. (previously presented) The receiver circuit according to claim 32, further comprising:

an integrated control circuit having a control terminal, said package having a terminal pin forming said control terminal.

34. (new) An optical receiver circuit, comprising:

a differential amplifier including a first input and a second input;

an optical reception device connected to said first input of said differential amplifier by a first preamplifier;

a dummy optical reception device connected to said second input of said differential amplifier by a second preamplifier; and

an integrated control circuit adapted to enable a user to externally control transimpendance characteristics of the first and second preamplifiers.